

ABSTRACT OF THE DISCLOSURE

In a non-contact IC card for performing communication with a reader/writer, an encryption block having a DES (data encryption standard) block for performing an encryption operation in accordance with a public key encryption algorithm and an ECC/SHA1 (elliptic curve cryptosystem/secure hash algorithm 1) block for performing an encryption operation in accordance with a common key encryption algorithm are incorporated into an IC chip. A CPU having a built-in clock gear causes a change of the operation frequency of the non-contact IC card. The communication operation with the reader/writer and the encryption operations performed by the encryption block are performed at different operation frequencies.